

TUESDAY May 13

Academic and Industrial Research: Using the Internet and IEEE's XPLORE Database

Speaker: Paul Wesling, CPMT Society Distinguished Lecturer & SF Bay Area Council Communications Director and GRID editor
Time: Social/Pizza at 4:00 PM; Presentation at 4:30 PM, Q&A at 5:30 PM
Cost: \$5 donation for pizza (free, for students)
Place: DeVry University Room 101, 6600 Dumbarton Circle, Fremont
RSVP: not required; for additional information: Mostafa Mortezaie, mmorteza@devry.edu (510) 574-1132
Web: www.e-grid.net/docs/0805-oeb-devry.pdf

Paul Wesling received his BS in electrical engineering and his MS in materials science from Stanford University. Following assignments at GTE/Lenkurt Electric (component engineering), ISS/Sperry Univac (bubble memory development, reliability, manufacturing engineering), Datapoint Peripheral Products (VP - Product Integrity), and Amdahl (design analysis, mainframe testing, console peripherals), he joined Tandem Computers (now HP's NonStop Enterprise Division) in 1985. As a member of the development team for advanced IC packaging, he designed several multi-chip module prototypes. In Tandem's Education Group from 1993-2002, he developed courses on reliability, managed Tandem's Distinguished Lectures series, and was on education's Technology Initiative team. He organized a number of advanced technology and professional skills development courses for his Division and also for the IEEE. He managed a grant from the National Science Foundation for the development of multimedia educational modules in the field of IC packaging. Now retired, he is communications director for the IEEE's SF Bay Area Council and editor of its GRID Magazine.

As CPMT's vice president of publications for 22 years, he supervised four archival journals and a newsletter, and oversaw authors for IEEE Press books. He now serves as CPMT Society webmaster. He is a Fellow of the IEEE, and received the IEEE Centennial Medal, the CPMT Board's Distinguished Service award, the Society Contribution Award, and the IEEE's Third Millennium Medal. He has organized over 500 courses for the local IEEE chapter in the Santa Clara Valley.

ABSTRACT: IEEE is the largest publisher of electrotechnology literature in the world, and most results in our fields are extensively reported there. After a brief overview of IEEE's journals in computing, communications, packaging and other technical areas, the presentation will demonstrate methods for using the IEEE's on-line database of journal and conference papers for analysis of published results that can affect and influence the direction of development activities. This includes how to access the 1.6 million items using XPLORE, some search and selection strategies, full-text versus abstracts searches, storing searches for re-use, and using Google Scholar and Scitopia. Specific recommendations for accessing the full papers will be made. Both slides and actual online searches will be used to demonstrate various approaches that can be explored to find relevant papers, determine which labs/companies have teams working on specific aspects of the field, and the names of key researchers who might be contacted for assistance and information.



Accessing a portfolio of relevant citations can make a great difference in a university's research project or in a company's development directions. It can help inform decisions that must be made about allocating resources, and help reveal development directions that have already been tried, issues to be

considered, experimental methods that have worked, etc. Maintaining access to IEEE's literature can be a key differentiating factor for your career as an engineer, developer or manager.

Both students and working engineers/managers are welcome to attend this talk, on the DeVry campus.