

Tom Coughlin's Welcome to Region 6 - page 7 CHAPTER MEETINGS

SCV-SPS - **8/6** | Real-world Audio Source Separation - automatic karaoke, extracting dialogue from old films, upmixing mono recordings to multi-channel recordings, Algorithmically, an ill-posed and challenging problem ... (AMD, Sunnyvale, 6:30 pm)

SCV-REL - 8/6 | Macroscopic & Stochastic
Aspects of Negative Bias Temperature
Instability - shift in MOSFET parameters, defect
generation explained from both macroscopic and
stochastic viewpoints ... (Qualcomm, Santa Clara, 6:00 pm)

SCV-PHOTO - **8/11** | Networking in the Data center (Intel, Santa Clara, 6 pm)

SCV-EDS - **8/11** | <u>2D Crystals for Smart Life</u> - 2D crystals for..., green electronics needed to support Big Data, IoT/IoE and social media ... (TI, Santa Clara, 6 pm)

SCV-CPMT - 8/12 | Robust 3DIC Package
Assembly Process Engineering for High
Volume Production - FPGA, CoWoS (Chip-On-Wafer-On-Substrate), low power and latency,
manufacturing complexity... (TI, Santa Clara, 11:30 am)

SCV-PV - **8/12** | Functional Coatings for PV Glass - very low-cost anti-reflective coatings, engineered to survive high temperatures, humidity, abrasion, soiling ... (PARC, Palo Alto, 6:30 pm)

SCV-TEMS - 8/13 | Understanding an Industry Technical Staff Pipeline - how the corporate staffing system works, criteria are used for career advancement ... (Webinar, 8 am)

Career Development

PROFESSIONAL DEVELOPMENT COURSES
Besser Associates - RF and Wireless
Training

UCSC Extension in Silicon Valley -Engineering Classes Ozen Engineering, Inc. - ANSYS training

EMPLOYMENT OPPORTUNITIES

Sonoma State University, Department of Engineering Teaching positions

CONFERENCE CALENDAR

Flash Memory Summit, August 11-13 2015, Santa Clara Convention Center

Hot Chips 27-A Symposium on High-Performance Chips, August 23-25, 2015, Flint Center, Cupertino

Hot Interconnects 23: A Symposium on High Performance Interconnects August 26-28, 2015, Oracle Santa Clara

<u>Interdrone</u> September 9-11, 2015, Las Vegas, Nevada

Berkeley Symposium on Energy Efficient Electronic Systems, October 1-2, 2015, Berkeley

Rock Stars of Cybersecurity October 27, 2015. San Jose

Rock Stars of Tech Startups November 10, 2015. San Francisco

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SCV-CS - 8/18 | Big Data - Yesterday, Today, and Tomorrow - From Hollerith machines and punch cards, Insights for the future can be gained by..."stack wars", applications ... (Cadence, San Jose, 6:30 pm)

SCV-NANO - 8/18 | Electrostatic Functionalization of Carbon Based Nanomaterials and Applications in Chemical, Gas and Bio-Sensing - electrostatic charging for the functionalization of carbon based nanomaterials, thin films ... (TI, Santa Clara, 11:30 am)

SF-PES - 8/19 | Advanced Smart
Grid Communications &
Applications: A Systems View
for Successful Deployment advanced wireless communication
system, utility monitoring,
communication, and control of
distribution equipment ... (PUC, SF,
11:30 am)

SCV-SPS - 8/19 | Scene
Understanding in the Era of
Deep Learning - Recognition of
3D objects, 3D reconstruction,
category-specific priors, "deep
learning" ... (AMD, Sunnyvale, 6:30 pm)

SCV-SSCS - 8/20 | Selected
Analog Design Topics a Two
Part Short Course - Modem for
high speed communications, 5um
down to 16nm, Time-based
circuits, efficient protocol for
encoding edge time information

... (TI, Santa Clara, 4 pm)

Computer /BAMMF - 8/21 | Bay Area | Multimedia Forum - Vision-enhanced | Immersive Interaction with Touch Board (ViiBoard), future large-scale video/light-field displays, ... (George E. Pake Auditorium, Palo Alto, 1:30 pm)

SCV-CE - 8/25 | 3D Printed Package
Components for High Performance
Packaging - print complex structures with
multi-material inks, mechanics of the
technology and manufacturing platform,
package types such as SiP, PoP... (NVIDIA,
Santa Clara, 6:30 pm)

SCV-CPMT - 8/27 | Packaging Solutions and Innovations with Compression

Molding - large-area-size substrates, limited by current transfer mold, slow "dipping" of the devices into either a melted granular or liquid mold compound... (TI, Santa Clara, 11:30 am)

SCV-SPS - 9/3 | Optical Character
Recognition for Most of the World's
Languages - recorded information, visual
rather than digital Unicode form, conversion
to Unicode for use in the digital world ...(AMD,
Sunnyvale, 6:30 pm)

SCV-CPMT - 9/9 | Anisotropic
Conductive Film (ACF) Interconnection
Technology for Wearable Electronics
Applications - flexible printed circuits
(FPCs) substrates, COF (Chip On Flex)/
CIF (Chip In Flex), FOF (Flex On Flex) and
FOF (Flex On Fabric) technologies ... (TI,
Santa Clara, 5:45 pm)

SCV-COMSOC - 9/9 | 5G: Expectations and the Road Ahead path to full 5G feature set network, optimized derivative LTE versions for various vertical markets ... (TI, Santa Clara, 6:30 pm)

SF-PES - 9/16 | Understanding the Electrical Arcing and the Arc-flash Hazard Study - theory of the electrical arcing and the accepted methods to calculate the arc-flash hazard, develop strategies to minimize burn injuries during an arc flash incident ...(PUC, SF, 11:30 am)

SCV-SPS - **10/15** | Gathering Light - prime lenses that provide an optical zoom, small mirrors allow reconfiguring the cameras to select.., architecture of this flexible computational camera ... (AMD, Sunnyvale, 6:30 pm)

SCV-MTT - 10/17 | Short Course 2015 - RF and EMI/EMC: Instrumentation, Components and Practice - fundamentals of RF technology such as Maxwell's equations, electromagnetic theory, Sparameter methods, time and frequency domain measurement techniques, key components (amplifier, filter, and mixer) utilized in the RF and EMC ... (TBD, 8 am - 5 pm)

SF-PES - 10/21 | The Future of Energy: Smart Grid and Beyond - Fall Banquet, roadmap, Three key visionary concepts, solar PV, Intelligent Electronic Device (IED) integration

... (Jillian's at Metreon, SF, 6:30 pm)



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Be in San Francisco on November 10, 2015, at the Village in San Francisco.

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http://www.computer.org/web/rock-stars/tech-startups

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Keynotes
Micron • NetApp
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Toshiba • Toyota
Kaminario • Tegile

"Solid state drives will be the biggest change in storage, a total game-changer, and flash will be the dominant type of SSD for the foreseeable future."

Joe Tucci, EMC

Features

Major Exhibitors
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August 11-13 2015

Santa Clara Convention Center FREE PARKING

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4th Berkeley Symposium on Energy Efficient Electronic Systems

October 1-2, 2015 Berkeley, CA, USA



Robert Colwell
Former Director
Microsystems Technology
Office
DARPA

Banatao Auditorium, Sutardja Dai Hall UC Berkeley 2594 Hearst Avenue Berkeley, California

Learn about the radical reduction in power consumption in these areas:

Ultra Low Voltage Nanoelectronics
Milli-Volt Nanomechanical Logic
Ultra-Low Energy Spintronics
Optical Chip Scale Interconnects
Low Voltage CMOS Circuits and Architectures
Energy Efficient Computing Systems

Jeffrey Hawkins

Co-Founder
Palm Computing &



Bringing together researchers who are working on breakthrough improvements in energy efficiency for information processing systems. The goal is for the attendees to gain an integrated perspective of the challenges and advances in this domain of technology through sessions that cover the entire food chain, from devices to systems. **For more information**, please visit the Symposium website: http://www.e3s-center.org/symposium IEEE registration discounted to \$225 before September 3rd, \$250 after September 3rd.

KEYNOTE SPEAKERS

Jeffrey Hawkins, Co-Founder, Palm Computing and Handspring
Robert Colwell, Former Director, DARPA's
Microsystems Technology Office
Other Speakers include: Suman Datta, Penn
State U., D. Scott Holmes, IARPA, Ali Javey, UC
Berkeley, Pedram Khalili, UCLA, Subhasish
Mitra, Stanford U., Dharmendra Modha, IBM,
Peter De Dobbelaere, Luxtera Inc., Enrico
Sangiorgi, U of Bologna, Shinichi Takagi, U. of
Tokyo, Michael Watts, MIT, Ming C. Wu, UC
Berkeley, Jun-Bo Yoon, KAIST, Jimmy Zhu,
Carnegie Mellon U., 12 Contributed Talks, 3

panels, and Poster Session with 20+ presenters

Learn about the radical reduction in power consumption in these areas:

- Ultra Low Voltage Nanoelectronics
- Milli-Volt Nanomechanical Logic
- Ultra-Low Energy Spintronics
- Optical Chip Scale Interconnects
- Low Voltage CMOS Circuits and Architectures
- Energy Efficient Computing Systems

Message from the new IEEE Region 6 Director

IEEE Region 6 is a wide geographical area with diverse interests. As a consequence, we have many opportunities and challenges that face our members. We also have enormous resources, both physical and human, in Region 6. Over the last few years we have developed a series of annual conferences focusing on the IEEE motto of "Advancing Technology for Humanity." These include our Global Humanitarian Technology Conference (GHTC) and the Technologies for Sustainability Conference (SusTech).

Over the next couple of years we are considering expanding our collection of conferences to include an Industrial Technology Conference and a Patent and Intellectual Property Conference. We are interested in what sort of events would interest you, so please let us know. Also if you are excited about getting engaged in these events we are always looking for committee members to make them successful, so let the chairs of these Conferences know if you want to get involved.

In any case, please consider coming to these events and/or letting your friends and colleagues know about them. We hope that you can come to one or more of these events and participate in the growth and development of our IEEE Region 6.

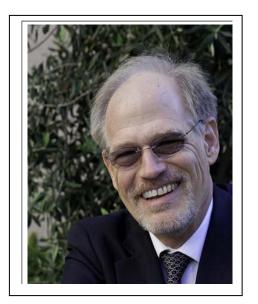
Best wishes.

Tom Coughlin

IEEE Region 6 Director 2015-16

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More messages from Tom in the following pages.



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